

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	80	(712/21).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/11/21 09:11
L2	537	(712/215).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/11/21 09:11
L3	221	(712/206).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/11/21 09:11
L4	476	(712/210).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/11/21 09:12
L5	382	(712/209).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/11/21 09:12
L6	8	(instruction adj1 width) same (valid\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 09:12
L7	7	(width near4 bit\$1) near4 instruction near4 valid\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 09:12
L8	3	emulation same (width adj1 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:22
L9	7	vliw same (variable adj1 width)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:22
L10	14	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) with (width near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:22
L11	0	((instruction near4 emulation) near4 register\$1) with (bit\$1 near4 width\$1).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:39
L12	1	((instruction with emulation) with register\$1) with (bit\$1 with width\$1).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:39
L13	1	((instruction with emulation) with register\$1) same (bit\$1 with width\$1).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:39
L14	1	((instruction same emulation) same register\$1) same (bit\$1 same width\$1).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:40

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L16	2	((instruction same emulation) same register\$1) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:40
L17	0	((run?test\$3 near4 idl\$3) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:40
L18	0	((run?test\$3 same idl\$3) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:40
L19	1	((test\$3 same idl\$3) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:40
L20	1	((run same test\$3 same idl\$3) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:40
L21	8	((run same test\$3) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:40
L22	0	((instruction near4 emulation) near4 register\$1) with (bit\$1 near4 width\$1)).clm.	US-PGPUB; USPAT	OR	OFF	2007/11/21 10:40
L23	1	((instruction with emulation) with register\$1) with (bit\$1 with width\$1)).clm.	US-PGPUB; USPAT	OR	OFF	2007/11/21 10:40
L24	1	((instruction with emulation) with register\$1) same (bit\$1 with width\$1)).clm.	US-PGPUB; USPAT	OR	OFF	2007/11/21 10:40
L25	1	((instruction same emulation) same register\$1) same (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT	OR	OFF	2007/11/21 10:40
L26	2	((instruction same emulation) same register\$1) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT	OR	OFF	2007/11/21 10:40
L27	0	((run?test\$3 near4 idl\$3) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT	OR	OFF	2007/11/21 10:41
L28	0	((run?test\$3 same idl\$3) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT	OR	OFF	2007/11/21 10:41
L29	1	((test\$3 same idl\$3) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT	OR	OFF	2007/11/21 10:41
L30	1	((run same test\$3 same idl\$3) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT	OR	OFF	2007/11/21 10:41

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L31	8	((run same test\$3) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT	OR	OFF	2007/11/21 10:41
L32	0	((run?test\$3) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT	OR	OFF	2007/11/21 10:41
L33	308	((test\$3) and (bit\$1 same width\$1)).clm.	US-PGPUB; USPAT	OR	OFF	2007/11/21 10:41
L34	99	("6163836" "6546480" "6574651" "6629115" "6789184" "7069420" "7082516" "20030172254" "20060149928" "6298438" "6393158" "20030172346" "5838984" "5933360" "6058465" "6138229" "6209077" "5339416" "5442580" "5590291" "5764787" "6202143" "20040244000" "20040250254" "5948053" "5440747" "6823505" "5333259" "5913038" "5974224" "5638533" "5671020" "5784572" "5845112" "6044206" "5530661" "5742840" "5799138" "5798753" "5220325" "5638531" "5768429" "5794061" "5794060" "5809321" "5822603" "5835793" "5872965" "5892966" "5926608" "5931934" "5954786" "5978546" "6018351" "6073150" "6088357" "6201536" "6226247" "6226247" "6230278" "6272615" "6279045" "6282631" "6292814" "6308253" "6310921" "6330666" "6334180" "6380983" "6397237" "6435737" "6437787" "6493005" "6535919" "6611269" "6748020" "6985783" "7099567" "7132963" "20020042801" "20020114610" "20020193893" "20030149780" "20040196905" "20060071829" "20070139228" "6209078" "5764698" "6094726" "6754808" "6760800" "6976151" "7035453" "7036000" "7155570" "20030169627" "20040151394" "20040210744" "5889917" "6091555").pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 11:14
S1	214	(712/24).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/06/14 07:55

EAST Search History

S2	65	(712/21).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/06/14 07:55
S3	171	(712/206).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/06/14 07:55
S4	275	(712/209).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/06/14 07:55
S5	379	(712/210).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/06/14 07:55
S6	431	(712/215).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/06/14 07:55
S7	0	VLIW same (emulation adj1 register)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/09/05 14:25
S8	64	VLIW same (instruction adj1 register)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/09/05 14:33
S9	13	VLIW same (instruction adj1 register) same pipeline	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/09/05 14:32
S10	0	emulation adj1 instruction adj1 register	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/09/05 14:32
S11	21	emulation near4 instruction adj1 register	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/09/05 14:32
S12	21	emulation near4 (instruction adj1 register)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/09/05 14:32

EAST Search History

S13	2	VLIW same (instruction adj1 register) same valid\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/09/05 14:34
S14	19	VLIW same (valid\$3 near3 instruction)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/09/05 14:38
S15	3	(("5941980") or ("4858105") or ("6065108")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2003/09/05 14:38
S16	0	emulation near4 (width adj1 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:48
S17	1	emulation with (width adj1 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 17:00
S18	0	(valid near4 (width adj1 bit\$1)) near4 instruction	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:50
S19	12	(width near4 instruction) near4 valid\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:52
S20	2	(width near4 bit\$1) near4 instruction near4 valid\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:54
S21	0	(width adj1 bit\$1) near4 (instruction adj1 valid\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:54
S22	0	(width adj1 bit\$1) same (instruction adj1 valid\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:54
S23	0	(width adj1 bit\$1) with (instruction adj1 valid\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:54
S24	0	(width adj1 bit\$1) with (instruction near4 valid\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:55

EAST Search History

S25	1	(width near2 bit\$1) with (instruction near4 valid\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:56
S26	0	(instruction adj1 width) near4 (instruction adj1 valid\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:56
S27	0	(instruction adj1 width) same (instruction adj1 valid\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:56
S28	0	(instruction adj1 width) with (instruction adj1 valid\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:57
S29	0	(instruction adj1 width) near4 (valid\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:57
S30	188455	(instruction adj1 width) sa,e (valid\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:57
S31	3	(instruction adj1 width) same (valid\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:59
S32	0	(instruction adj1 width) with (valid\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 16:59
S33	2	emulation same (width adj1 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 17:01
S34	1	VLIW near4 (width near4 bits)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 17:01
S35	4	VLIW with (width near4 bits)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 17:02
S36	4	(variable near4 instruction\$1 near4 length\$1) near4 (bit\$1 near4 width)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 17:12

EAST Search History

S37	68	(instruction near4 emulation) near4 register\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/11/21 10:25
S38	5	(instruction adj1 emulation) near4 register\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 17:17
S39	2	(instruction adj1 emulation) near4 (emulation near4 register\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 17:20
S40	3	(instruction adj1 emulation) with (emulation near4 register\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 17:27
S41	16	(instruction adj1 emulation) same (emulation near4 register\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/10 17:27
S42	229	(712/24).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 10:06
S43	73	(712/21).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 10:07
S44	176	(712/206).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 10:07
S45	291	(712/209).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 10:07
S46	414	(712/210).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 10:07
S47	451	(712/215).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 10:07

EAST Search History

S48	185	((test\$3 or debug\$4) near4 interfac\$3) near4 emulat\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 12:19
S49	0	((test\$3 or debug\$4) near4 interfac\$3) near4 emulat\$3 near4 (instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 10:10
S50	4	(((test\$3 or debug\$4) near4 interfac\$3) near4 emulat\$3) with instruction\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 10:46
S51	17	(((test\$3 or debug\$4) near4 interfac\$3) near4 emulat\$3) near4 (JTAG or (Joint adj1 Test adj1 action adj1 group))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 10:19
S52	21	(((test\$3 or debug\$4) near4 interfac\$3) near4 emulat\$3) with (JTAG or (Joint adj1 Test adj1 action adj1 group))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 12:20
S53	13	(((test\$3 or debug\$4) near4 interfac\$3) near4 emulat\$3) same instruction\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 10:52
S54	0	(((test\$3 or debug\$4) near4 interfac\$3) near4 emulat\$3) same (JTAG or (Joint adj1 Test adj1 action adj1 group)) same superscalar	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 12:44
S55	0	(((test\$3 or debug\$4) near4 interfac\$3) near4 emulat\$3) same (JTAG or (Joint adj1 Test adj1 action adj1 group)) same (superscalar or vliw or (very adj1 long adj1 instruction adj1 word) or (simultaneous adj1 issu\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 13:05
S56	2	(((test\$3 or debug\$4) near4 interfac\$3) near4 emulat\$3) same (JTAG or (Joint adj1 Test adj1 action adj1 group))) and (superscalar or vliw or (very adj1 long adj1 instruction adj1 word) or (simultaneous adj1 issu\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 13:27
S57	13	(((test\$3 or debug\$4) near4 emulat\$3) same (JTAG or (Joint adj1 Test adj1 action adj1 group))) and (superscalar or vliw or (very adj1 long adj1 instruction adj1 word) or (simultaneous adj1 issu\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 13:28

EAST Search History

S58	1	("5329471").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 14:27
S59	1	("5535331").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 15:03
S60	0	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (variable near4 width near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 15:04
S61	0	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) with (variable near4 width near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 15:04
S62	0	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) same (variable near4 width near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 15:04
S63	22	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) same (variable near4 width)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 16:09
S64	12	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (variable near4 width)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 15:59
S65	3	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (width near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 15:08
S66	8	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) with (width near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 15:09
S67	2	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (variable near4 width near4 instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 15:13
S68	2	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) with (variable near4 width near4 instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 15:13
S69	5	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) same (variable near4 width near4 instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 15:13

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S70	0	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) and (variable and width)	IBM_TDB	OR	OFF	2004/12/14 16:00
S71	0	vliw and (variable and width)	IBM_TDB	OR	OFF	2004/12/14 16:00
S72	0	vliw same (variable same width)	IBM_TDB	OR	OFF	2004/12/14 16:00
S73	4	vliw same (variable adj1 width)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 16:00
S74	25	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) same ((variable or different) near4 width)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/14 16:11
S75	0	(vliw or (very adj1 long adj1 instruction adj1 word)) near4 (multiplexer or mux or select\$3)	IBM_TDB	OR	OFF	2004/12/15 08:02
S76	0	(vliw or (very adj1 long adj1 instruction adj1 word)) with (multiplexer or mux or select\$3)	IBM_TDB	OR	OFF	2004/12/15 08:02
S77	0	(vliw or (very adj1 long adj1 instruction adj1 word)) near4 (multiplexer\$1 or mux\$2 or select\$3)	IBM_TDB	OR	OFF	2004/12/15 08:03
S78	96	(vliw or (very adj1 long adj1 instruction adj1 word)) near4 (multiplexer\$1 or mux\$2 or select\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 08:03
S79	25	(vliw or (very adj1 long adj1 instruction adj1 word)) near4 (multiplexer\$1 or mux\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 08:03
S80	70	(vliw or (very adj1 long adj1 instruction adj1 word)) near4 (multiplexer\$1 or mux\$2 or select\$3) near4 (instruction\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 08:04
S81	13	(vliw or (very adj1 long adj1 instruction adj1 word)) near4 (multiplexer\$1 or mux\$2) near4 (instruction\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/15 08:04
S82	243	(712/24).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/05/02 08:40

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S83	74	(712/21).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/05/02 08:40
S84	181	(712/206).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/05/02 08:40
S85	303	(712/209).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/05/02 08:40
S86	421	(712/210).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/08/19 19:25
S87	461	(712/215).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/05/02 08:40
S88	254	(712/24).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 14:39
S89	77	(712/21).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 14:39
S90	189	(712/206).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 14:39
S91	317	(712/209).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 14:39
S92	437	(712/210).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 14:39

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S93	475	(712/215).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 14:55
S94	0	(vliw) near4 ((start and stop) near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 14:55
S95	0	(vliw) same ((start and stop) near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 14:55
S96	7	(vliw) same (((start\$3 or begin\$4) and (stop\$4 or end\$3)) near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:13
S97	13	(vliw) same (((start\$3 or begin\$4) and (stop\$4 or end\$3)) with bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:21
S98	114	(vliw) same (((start\$3 or begin\$4) and (stop\$4 or end\$3)) same bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:22
S99	448	(vliw) same ((width or size\$1 or length\$1) same bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:22
S100	95	(vliw) with ((width or size\$1 or length\$1) with bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:23
S101	71	(vliw) with ((width or size\$1 or length\$1) near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:23
S102	40	(vliw) near4 ((width or size\$1 or length\$1) near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:46
S103	2193	(test\$3) near4 (interfac\$3) near4 (signal\$4 or bit\$1 or alert\$3 or indicat\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:47
S104	2069	(test\$3) near4 (interfac\$3) near4 (signal\$4 or indicat\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:47

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S10 5	67	(test\$3) near4 (interfac\$3) near4 (signal\$4 or indicat\$3) near4 stat\$2	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:48
S10 6	0	(test\$3) near4 (interfac\$3) near4 (signal\$4 or indicat\$3) near4 stat\$2 near4 emulat\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:48
S10 7	0	((test\$3) near4 (interfac\$3) near4 (signal\$4 or indicat\$3) near4 stat\$2) same emulat\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:48
S10 8	33	((test\$3) near4 (interfac\$3) near4 (signal\$4 or indicat\$3)) same emulat\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:52
S10 9	7	((test\$3) near4 (interfac\$3) near4 (signal\$4 or indicat\$3)) same emulat\$3 same instruction\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/10/13 15:53
S11 0	78	(712/21).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/08/19 19:24
S11 1	196	(712/206).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/08/19 19:24
S11 2	327	(712/209).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/08/19 19:24
S11 3	451	(712/210).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/03/31 10:06
S11 4	486	(712/215).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/08/19 19:25
S11 5	81	(712/21).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/02/26 16:16

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S11 6	211	(712/206).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/02/26 16:19
S11 7	358	(712/209).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/02/26 16:20
S11 8	467	(712/210).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/02/26 16:30
S11 9	519	(712/215).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/02/26 16:31
S12 0	4394	((test\$3 or debug\$4) near4 (signal\$5 or flag\$5 or bit\$1)) with instruction\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/04 18:38
S12 1	55	((test\$3 or debug\$4) near4 (signal\$5 or flag\$5 or bit\$1)) with (instruction\$1 near4 interfac\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/04 18:38
S12 2	932	(((test\$3 or debug\$4) near4 (signal\$5 or flag\$5 or bit\$1)) with instruction\$1).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/04 18:38
S12 3	12	(((test\$3 or debug\$4) near4 (signal\$5 or flag\$5 or bit\$1)) with (instruction\$1 near4 interfac\$3)). clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/04 18:39
S12 4	22	(valid\$5 with instruction\$1 with width with bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/04 18:40
S12 5	3	(valid\$5 with instruction\$1 with width with bit\$1).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/04 18:40
S12 6	0	((roth-charles\$) and (singh-ravi\$) and (overkamp-gregory\$) and (dinh-tien\$)).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/04 18:42

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S12 7	162	((roth-charles\$) or (singh-ravi\$) or (overkamp-gregory\$) or (dinh-tien\$)).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/04 18:43
S12 8	29	((roth-charles\$) or (singh-ravi\$) or (overkamp-gregory\$) or (dinh-tien\$)).in. and (intel\$ and (analog-device\$)).as.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/04 18:43
S12 9	80	(712/21).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/08/19 19:25
S13 0	218	(712/206).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/08/19 19:25
S13 1	372	(712/209).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/08/19 19:25
S13 2	471	(712/210).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/08/19 19:25
S13 3	531	(712/215).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/08/19 19:25
S13 4	8	((test\$3) near4 (interfac\$3) near4 (signal\$4 or indicat\$3)) same emulat\$3 same instruction\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/19 19:26
S13 5	49	((test\$3) near4 (interfac\$3) near4 (signal\$4 or indicat\$3)) same emulat\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/19 19:26
S13 6	51	(vliw) near4 ((width or size\$1 or length\$1) near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/19 19:27
S13 7	8	(vliw) same (((start\$3 or begin\$4) and (stop\$4 or end\$3)) near4 bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/19 19:27
S13 8	29	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) same (variable near4 width)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/19 19:28